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(54) **A CMOS active pixel digital camera**

(57) An image sensor device comprising a silicon substrate having a plurality of CMOS circuit formed thereon, including a pixel array having a plurality of rows and a plurality of columns, a row addressing circuit operatively connected to each the pixel array and the timing control circuit, the row addressing circuit having a row bus that provides address lines to each row in the pixel array, a column addressing circuit, a pixel timing circuit, a timing control logic block, a signal processing circuit, and an interface circuit coupled to external computational means for provision of address and control signals to the sensor device, the interface circuit being operatively coupled the timing control logic, the pixel timing circuit, the row addressing circuit and the column addressing circuit.

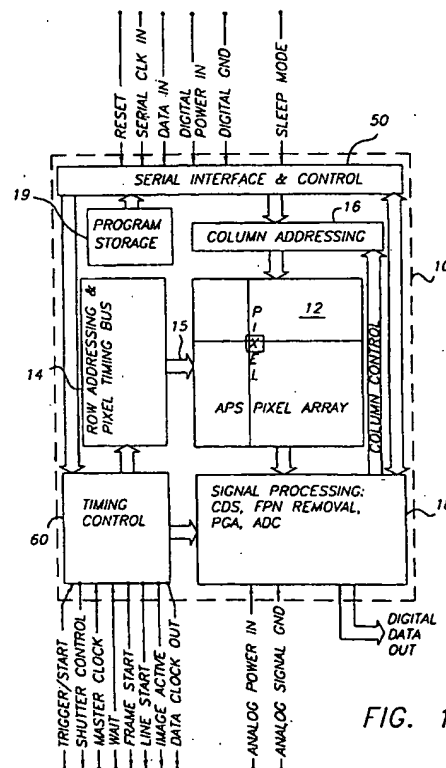


FIG. 1



European Patent
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EUROPEAN SEARCH REPORT

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EP 99 20 0052

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
X	NIXON R H ET AL: "256X256 CMOS ACTIVE PIXEL SENSOR CAMERA-ON-A-CHIP" IEEE INTERNATIONAL SOLID STATE CIRCUITS CONFERENCE,US,IEEE INC. NEW YORK, vol. 39, 1 February 1996 (1996-02-01), pages 178-179,440, XP000685585 ISSN: 0193-6530 * the whole document *	1-10	H04N3/15 H04N5/235
X	FOSSUM E R: "DIGITAL CAMERA SYSTEM ON A CHIP" IEEE MICRO,US,IEEE INC. NEW YORK, vol. 18, no. 3, 1 May 1998 (1998-05-01), pages 8-15, XP000755752 ISSN: 0272-1732 Chapter "System on a Chip", figures 6,7 Chapter "Rapid rate of progress"	1,2,5-10	
A	NAKAMURA J ET AL: "ON-FOCAL-PLANE SIGNAL PROCESSING FOR CURRENT-MODE ACTIVE PIXEL SENSORS" IEEE TRANSACTIONS ON ELECTRON DEVICES,US,IEEE INC. NEW YORK, vol. 44, no. 10, 1 October 1997 (1997-10-01), pages 1747-1758, XP000703889 ISSN: 0018-9383 * figure 13 *	1-10	TECHNICAL FIELDS SEARCHED (Int.Cl.6) H04N
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 15 December 2000	Examiner De Paepe, W
CATEGORY OF CITED DOCUMENTS X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: intermediate document		T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons &: member of the same patent family, corresponding document	

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